

# ANALOG WAY MIDRA

## Module: VIDEO INPUT

### AMX NETLINX

Date: **October 16, 2017**  
Driver version: **V1.11**  
Tested with: **Midra Firmware v02.00.15**

## INTRODUCTION

This is an optional module for controlling Midra series switchers. This module allows you to manage Midra switcher inputs:

- Input validity
- Input plugs management
- Input auto-Setting
- Input auto-Center
- Input misc. status



## IMPLEMENTATION

To interface this module in an AMX program, the programmer must perform the following tasks:

- Edit the file Midra\_User\_Definitions.axi: If the VIDEO\_INPUT module is used in the main program then you must assign the value 1 to the variable Midra\_Video\_Input\_Usage. If this is not the case, the value of this variable must remain at 0.
- Include the Midra\_Video\_Inputs module in the main program and adjust specific module parameters (see example program available with this package).

## COMMANDS

### Command Control

None

### Channels

The channels supported by the VIDEO\_INPUT module are listed below.

Channel code	Description
1..10	Read Input X properties (channel 1 for input 1, channel 2 for input 2, ...)
31..40	Start Input X auto-setting procedure (channel 31 for input 1, channel 32 for input 2, ...)
61..70	Start Input X auto-centering procedure(channel 61 for input 1, channel 62 for input 2, ...)
111..120	Pulse to freeze/unfreeze input X (channel 111 for input 1, channel 112 for input 2, ...)
255	Module initialization (automatically performed after being connected).



### Levels

The levels supported by the VIDEO\_INPUT module are listed below.

Level code	Description
1..10	Input X plug selection (level 1 for input 1, level 2 for input 2, ...). The level value is the plug number (0 to 4 – see table below)

## FEEDBACKS

### Channels

The channels supported by the VIDEO\_INPUT module are listed below.

Channel code	Description
121..120	Input X availability (channel 121 for input 1, channel 122 for input 2, ...)
151..160	Input X HDCP status (channel 151 for input 1, channel 152 for input 2, ...)
181..190	Input X signal detection (channel 181 for input 1, channel 182 for input 2, ...)
255	Module initialization status
301..305	Input1 plug 0 To 4selection status
311..315	Input2 plug 0 To 4 selection status
321..325	Input3 plug 0 To 4 selection status
331..335	Input4 plug 0 To 4 selection status
341..345	Input5 plug 0 To 4 selection status
351..355	Input6 plug 0 To 4 selection status
361..365	Input7 plug 0 To 4 selection status
371..375	Input8 plug 0 To 4 selection status
381..385	Input9 plug 0 To 4 selection status
391..395	Input10 plug 0 To 4 selection status



401..405	Input1 plug 0 To 4 availability status
411..415	Input2 plug 0 To 4 availability status
421..425	Input3 plug 0 To 4 availability status
431..435	Input4 plug 0 To 4 availability status
441..445	Input5 plug 0 To 4 availability status
451..455	Input6 plug 0 To 4 availability status
461..465	Input7 plug 0 To 4 availability status
471..475	Input8 plug 0 To 4 availability status
481..485	Input9 plug 0 To 4 availability status
491..495	Input10 plug 0 To 4 availability status

### Levels

The levels supported by the VIDEO\_INPUT module are listed below.

Level code	Description
31..40	Input X format (level 31 for input 1, level 32 for input 2, ...). The level value is the format code for the corresponding input (0 to 56) -> Cf. table below
61..70	Input X auto-centering progression (level 61 for input 1, level 62 for input 2, ...). -> values from 0 to 65535
81..90	Input X auto-setting progression (level 81 for input 1, level 82 for input 2, ...) -> values from 0 to 65535
101..110	Input X plug selected (level 101 for input 1, level 102 for input 2, ...). The level value is the plug code selected (0 to 4) -> Cf. table below

### Texts

The texts supported by the VIDEO\_INPUT module are listed below.

Address code	Description
1..10	Input X ASCII format from Midra device (address code 1 for input 1, address code 2 for input 2, ...) - Generally not used.
31..40	Input X signal width in pixels (address code 31 for input 1, address code 32 for input 2, ...)



61..70	Input X signal height in pixels (address code 61 for input 1, address code 62 for input 2, ...)
91..100	Input X format description from table below (address code 91 for input 1, address code 92 for input 2, ...). Preferred to Text 1..10.

### Input plugs

0	Plug of analog type (HD15 socket)
1	Plug of DVI type (Single or dual link)
2	Plug of SDI type
3	Plug of HDMI type
4	Plug of HDBaseT type

### Input formats

0	NONE
1	INVALID
2	UNKNOWN
3	SDTV NTSC
4	SDTV PAL
5	SDTV SECAM
6	SDTV 480i
7	SDTV 576i
8	EDTV 480p
9	EDTV 576p
10	HDTV 720p
11	HDTV 1035i
12	HDTV 1080i
13	HDTV 1080p
14	HDTV 2048x1080 Cinema
15	CEA861 720x240p
16	CEA861 720x288p
17	CPU 640x350
18	CPU 640x400
19	CPU 720x400
20	CPU VGA
21	CPU WVGA 5/3
22	CPU WVGA
23	CPU SVGA
24	CPU 1280x600



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25	CPU 720p RGB
26	CPU XGA
27	CPU WXGA
28	CPU SWXGA
29	CPU 1366x768
30	CPU 800p RGB
31	CPU SWXGA+
32	CPU 1152x864
33	CPU 900p RGB
34	CPU 1600x900
35	CPU 960p RGB
36	CPU SXGA
37	CPU SXGA3
38	CPU SXGA+
39	CPU WSXGA+
40	CPU 1080p RGB
41	CPU 2K
42	CPU QWXGA
43	CPU UXGA
44	CPU WUXGA
45	CPU 1792x1344
46	CPU 1856x1392
47	CPU 1920x1440
48	CPU WQHD
49	CPU QXGA
50	CPU WQXGA
51	CPU CVT Timing
52	CPU GTF Timing with 5/4 aspect ratio
53	CPU GTF Timing with 4/3 aspect ratio
54	CPU GTF Timing with 16/10 aspect ratio
55	CPU GTF Timing with 15/9 aspect ratio
56	CPU GTF Timing with 16/9 aspect ratio



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