ANALOG WAY MIDRA

Module: FRAME & LOGO

AMX NETLINX

Date: **October 16, 2017**
Driver version: **V1.11**
Tested with: **Midra Firmware v02.00.15**

INTRODUCTION

This is an optional module for controlling Midra series switchers. This module allows you to read configured logo/frames properties:

- Validity
- Dimensions

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IMPLEMENTATION

To interface this module in an AMX program, the programmer must perform the following tasks:

- Edit the file Midra_User_Definitions.axi: If the FRAME & LOGO module is used in the main program then you must assign the value 1 to the variable Midra_*Frame_Logo_*Usage. If this is not the case, the value of this variable must remain at 0.

- Include the Midra_Frame_Logo module in the main program and adjust specific module parameters (see example program available with this package).

COMMANDS

Command Control

None

Channels

The channel codes supported by the FRAME & LOGO module are listed below.

Channel code	Description
1..8	Read Frame X properties (channel 1 for frame 1, channel 2 for frame 2, …)
11..18	Read Logo Y properties (channel 11 for logo 1, channel 12 for logo 2 …)
255	Module initialization (automatically performed after being connected)

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FEEDBACKS

Channels

The channel codes supported by the FRAME & LOGO module are listed below.

Channel code	Description
31..38	Frame X validity (channel 31 for frame 1, channel 32 for frame 2, …)
51..58	Logo X validity (channel 51 for logo 1, channel 52 for logo 2, …)
255	Module initialization status

Texts

The texts supported by the FRAME & LOGO module are listed below.

Address code	Description
1..8	Frame X height (address code 1 for frame 1, address code 2 for frame 2, …)
11..18	Frame X width (address code 11 for frame 1, address code 12 for frame 2, …)
31..38	Logo X height (address code 31 for logo 1, address code 32 for logo 2, …)
41..48	Logo X width (address code 41 for logo 1, address code 42 for logo 2, …)